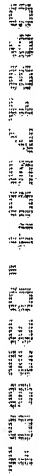


$\frac{d}{dt} \left(\frac{\partial L}{\partial \dot{x}} \right) = \frac{\partial L}{\partial x}$



F I G. 2B

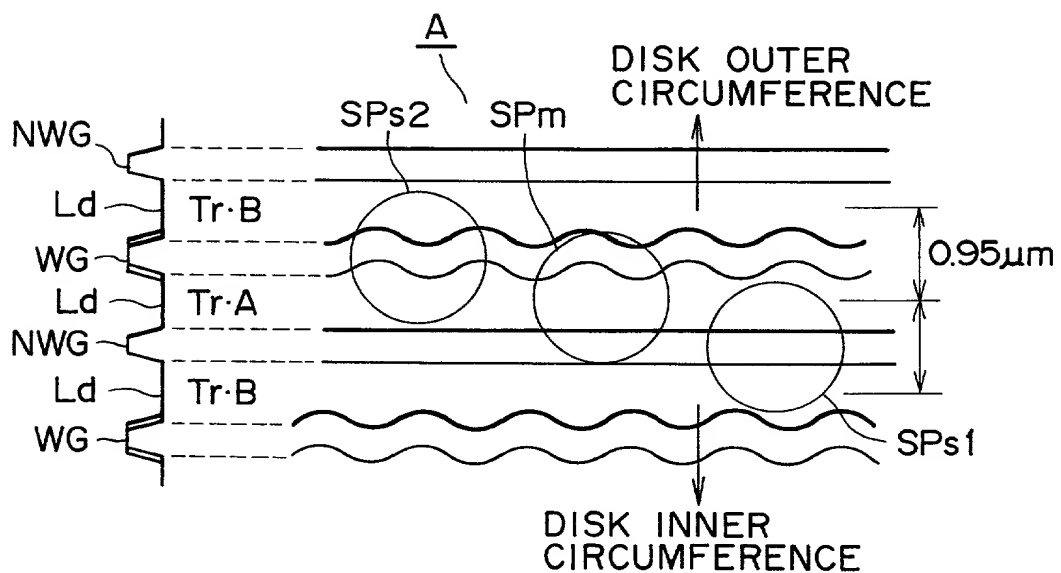


FIG. 3

	MD-DATA2	MD-DATA1
TORACK PITCH	0.95 μm	1.6 μm
PIT LENGTH	0.39 $\mu\text{m/bit}$	0.59 $\mu\text{m/bit}$
$\lambda \cdot \text{NA}$	650nm \cdot 0.52	780nm \cdot 0.45
RECORDING SYSTEM	LAND RECORDING	GROOVE RECORDING
ADDRESS SYSTEM	INTERLACE ADDRESSING (ONE SIDE WOBBLE OF DOUBLE SPIRALS)	OPPOSITE SIDE WOBBLES OF SINGLE SPIRAL CONVOLUTION
MODULATION SYSTEM	RLL(1,7)	EFM
ERROR CORRECTION SYSTEM	RS-PC	ACIRC
INTERLEAVE	BLOCK COMPLETED	CONVOLUTION
REDUNDANCY	19.7%	46.3%
LINEAR VELOCITY	2.0m/s	1.2m/s
DATA RATE	589kB/s	133kB/s
RECORDING CAPACITY	650MB	140MB

FIG. 5

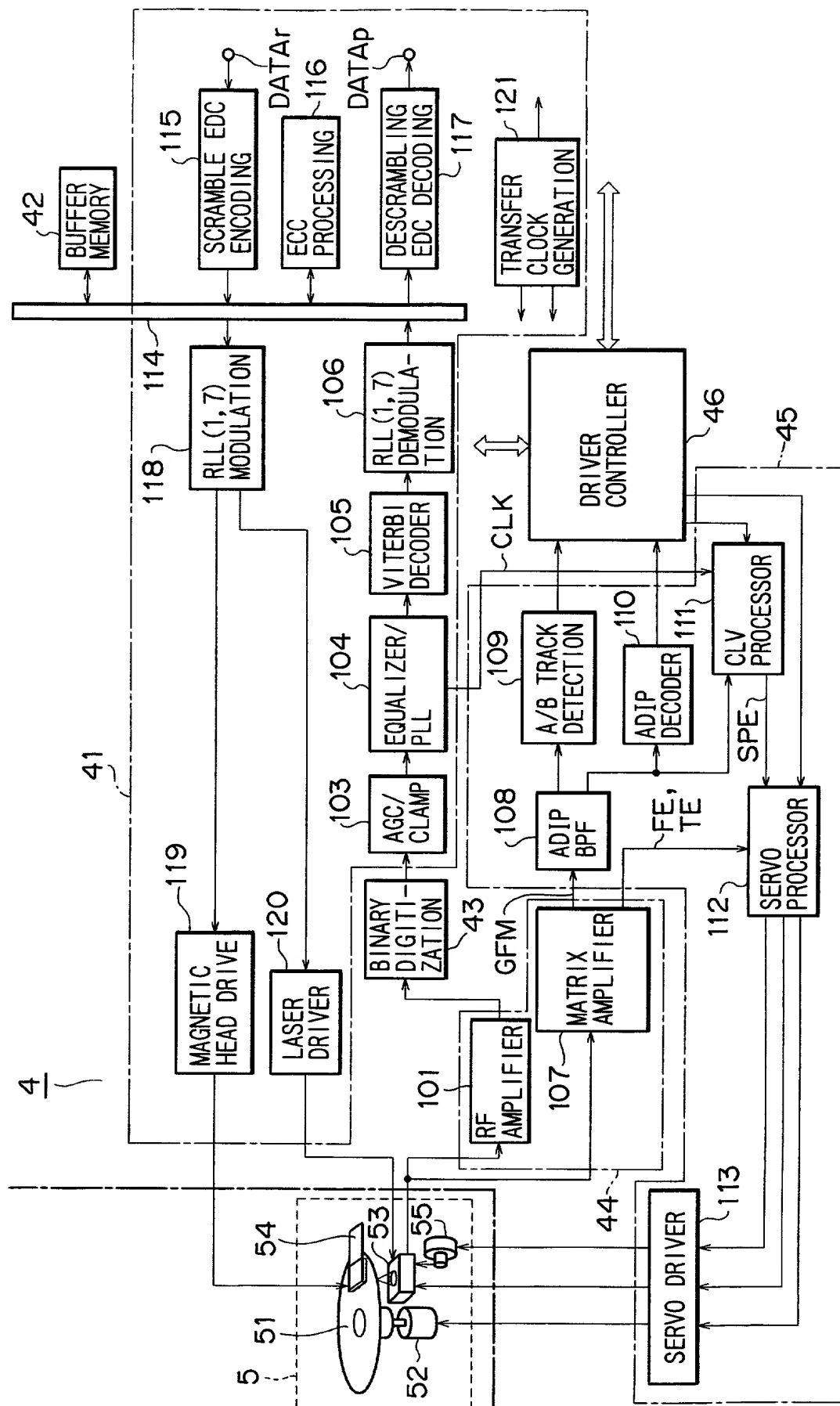


FIG. 6A

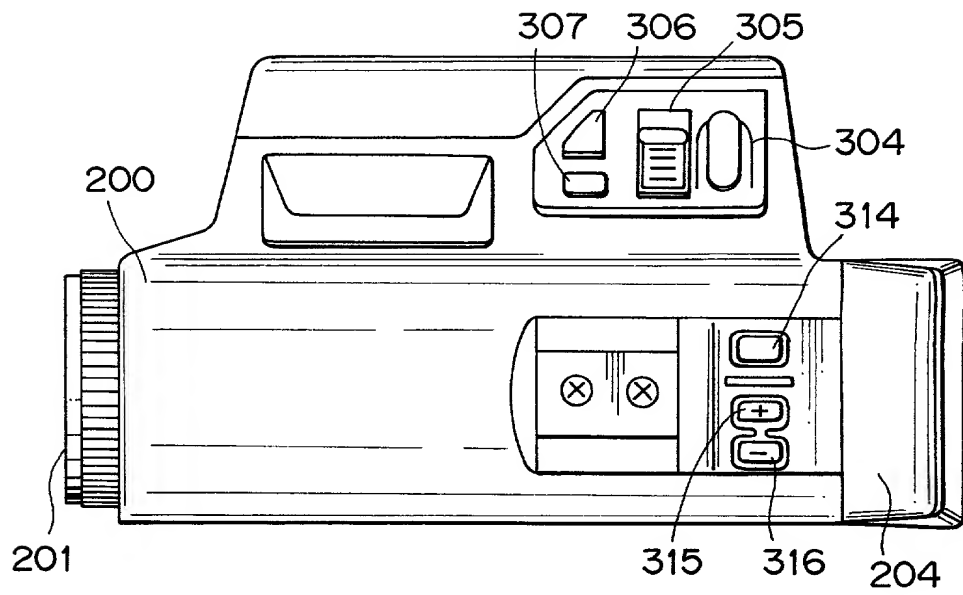


FIG. 6B

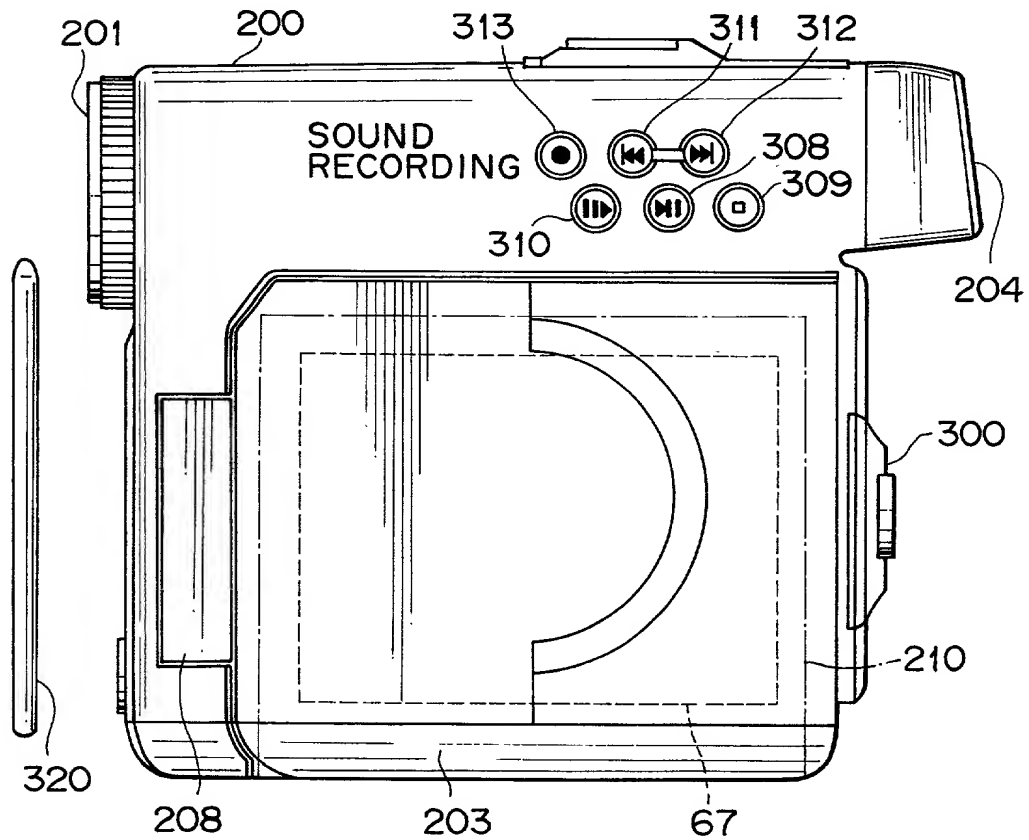


FIG. 7A

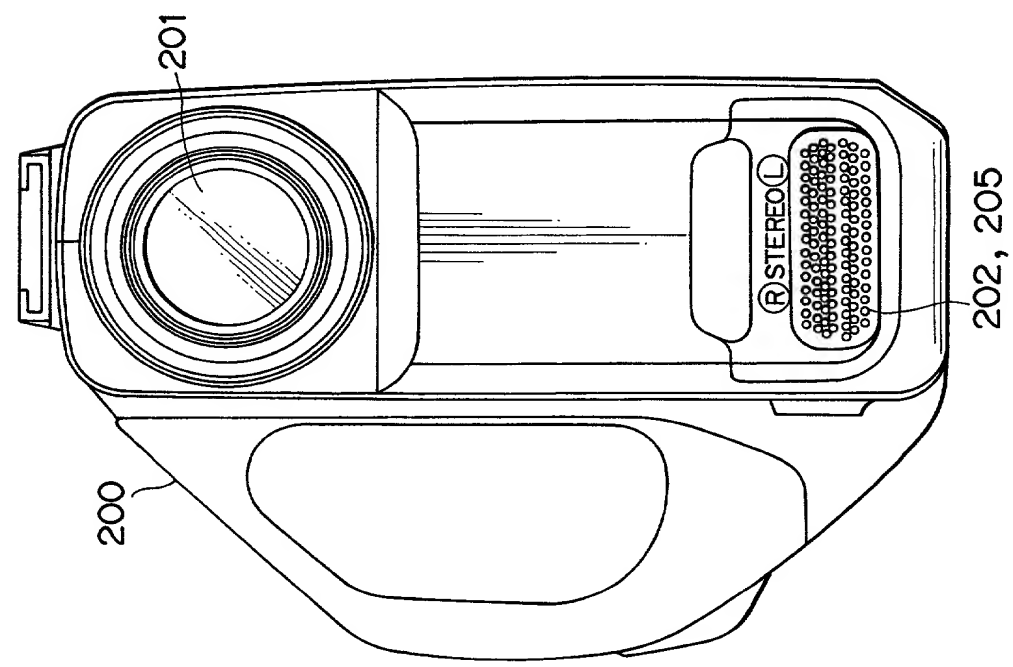


FIG. 7B

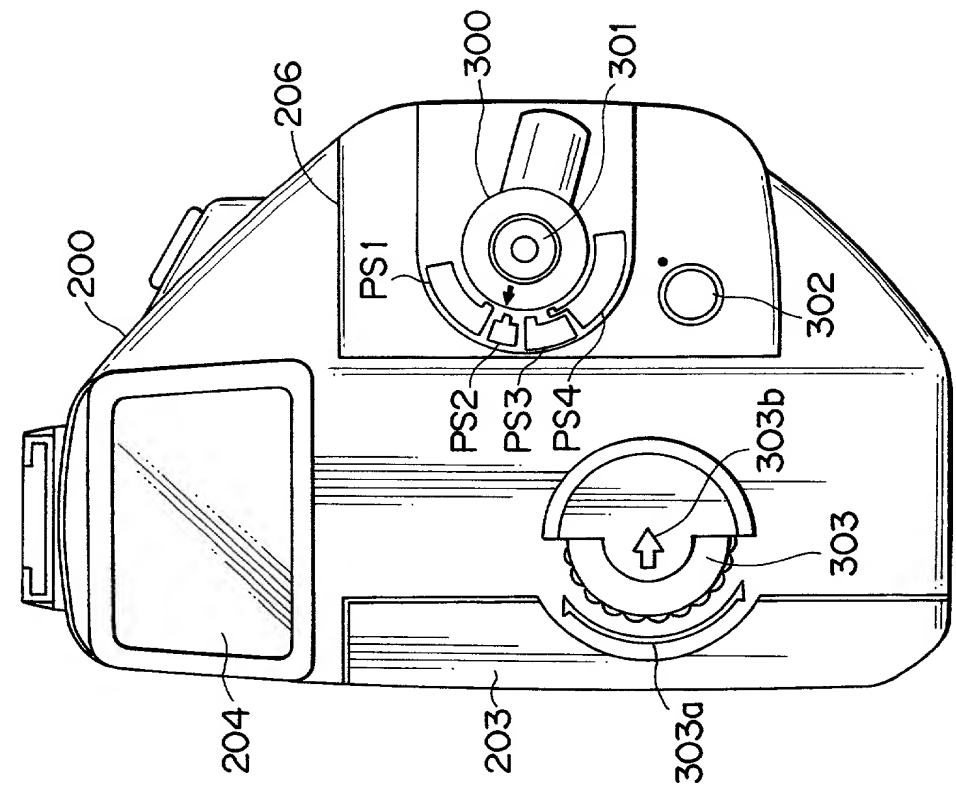


FIG. 8A

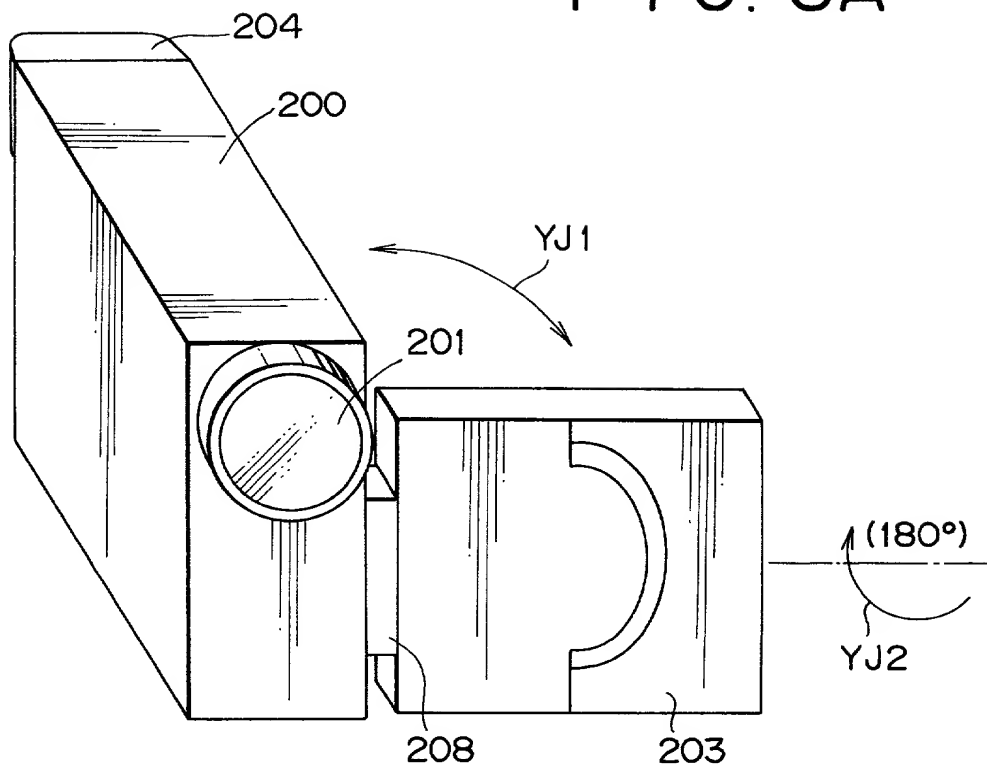


FIG. 8B

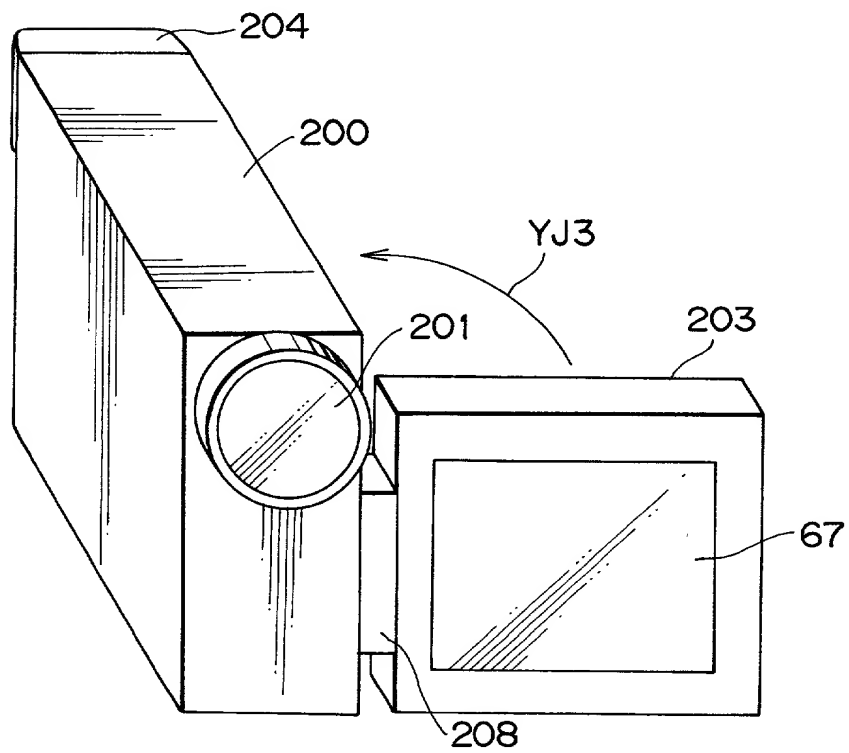
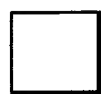
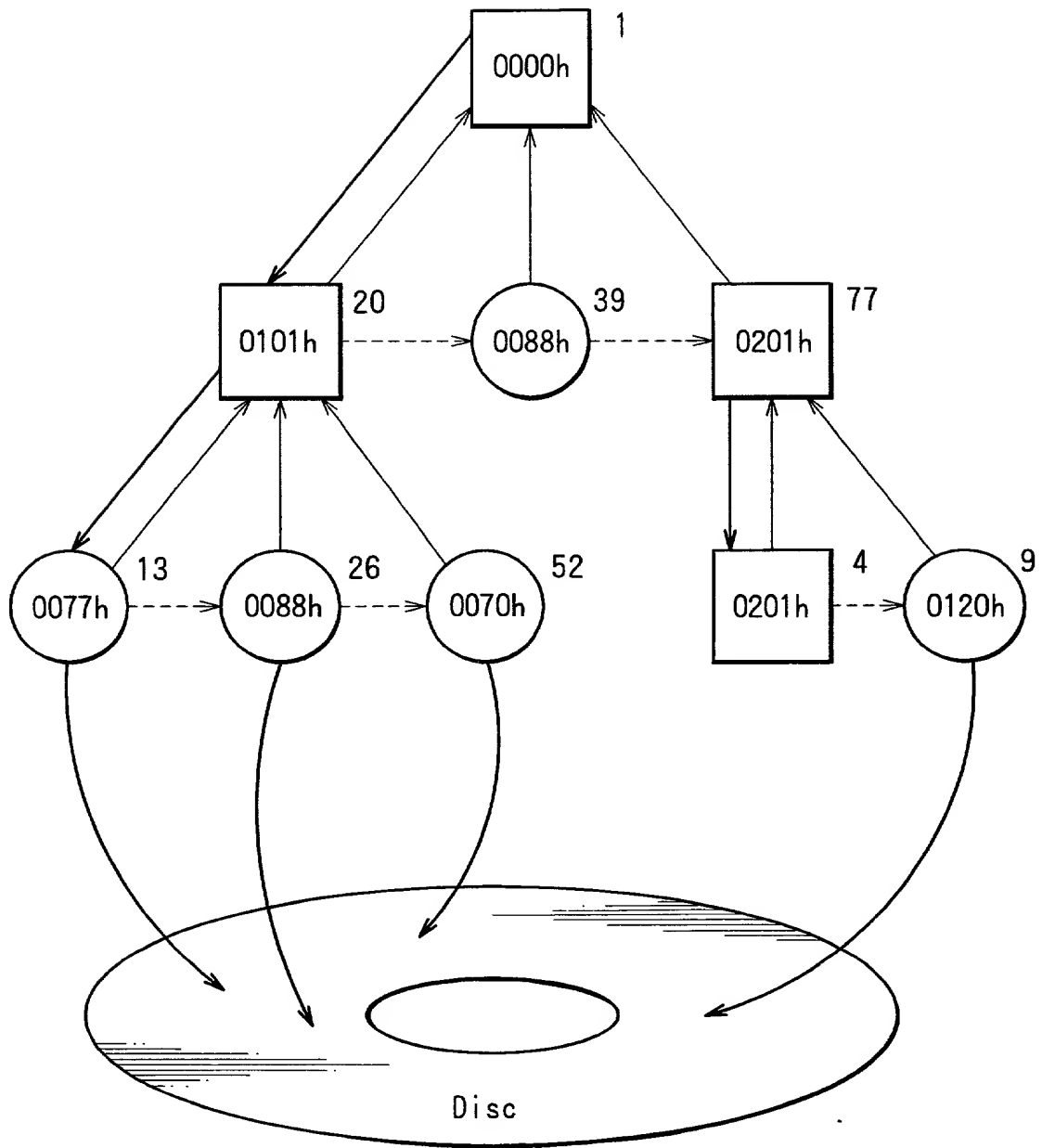


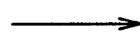
FIG. 9



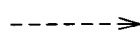
:Folder



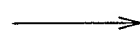
:Track



:Child Track/Data



:Next Track



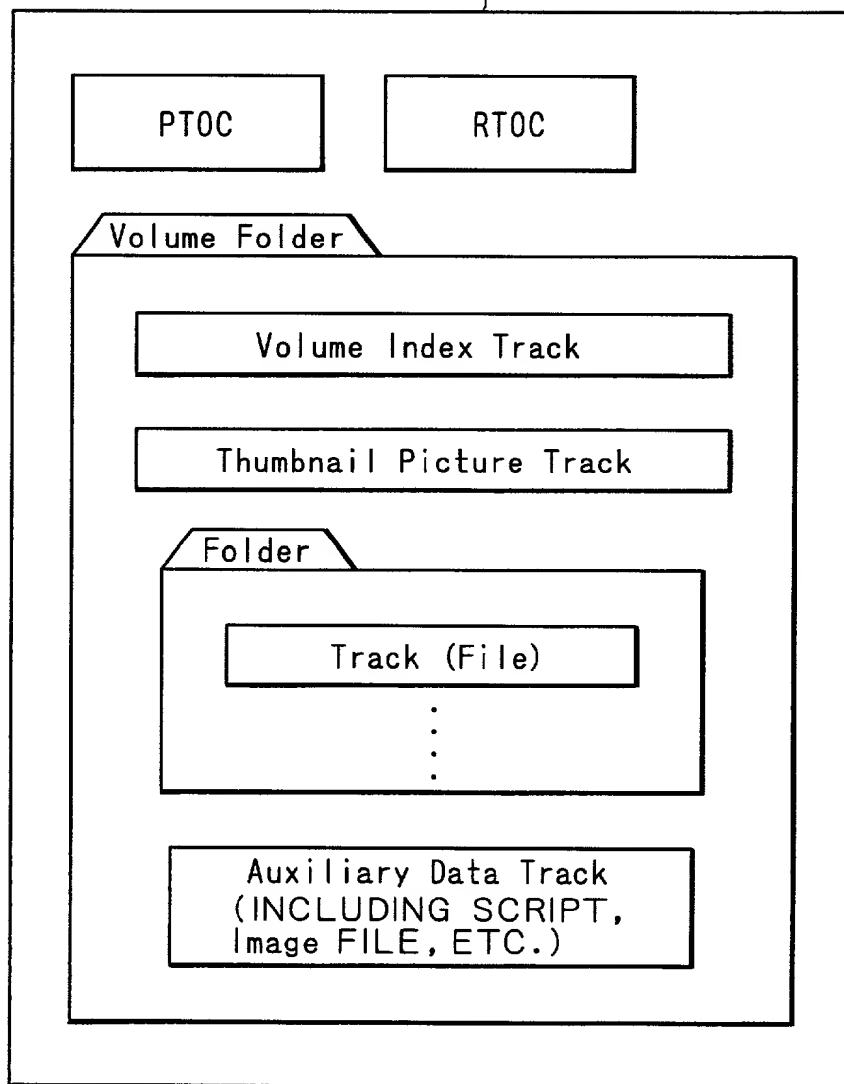
:Parent Folder

0070:ID

52:Descriptor Number

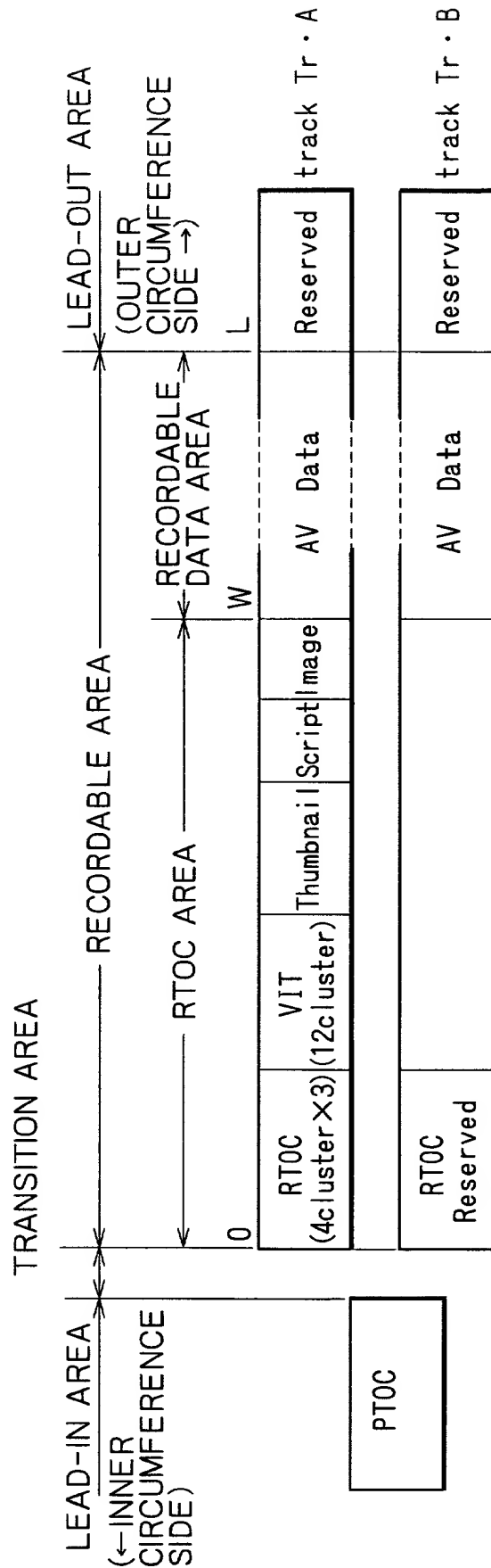
FIG. 10

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DATA STRUCTURE IN DISK

FIG.11



W: RECORDABLE DATA AREA START ADDRESS
L: LEAD-OUT AREA START ADDRESS

FIG. 12A



FIG. 12B



AV PACKET STRUCTURE

FIG. 14

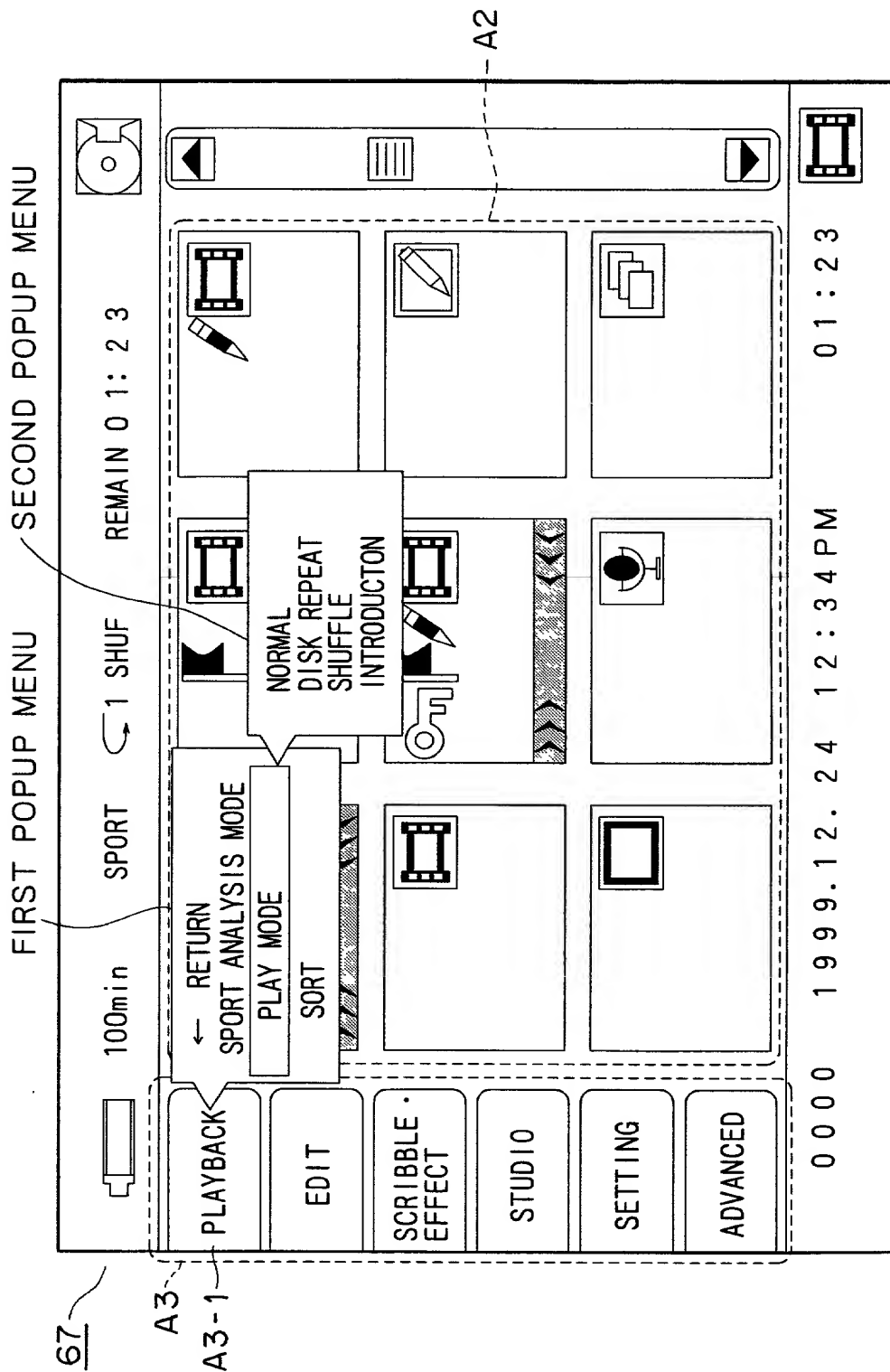


FIG. 15

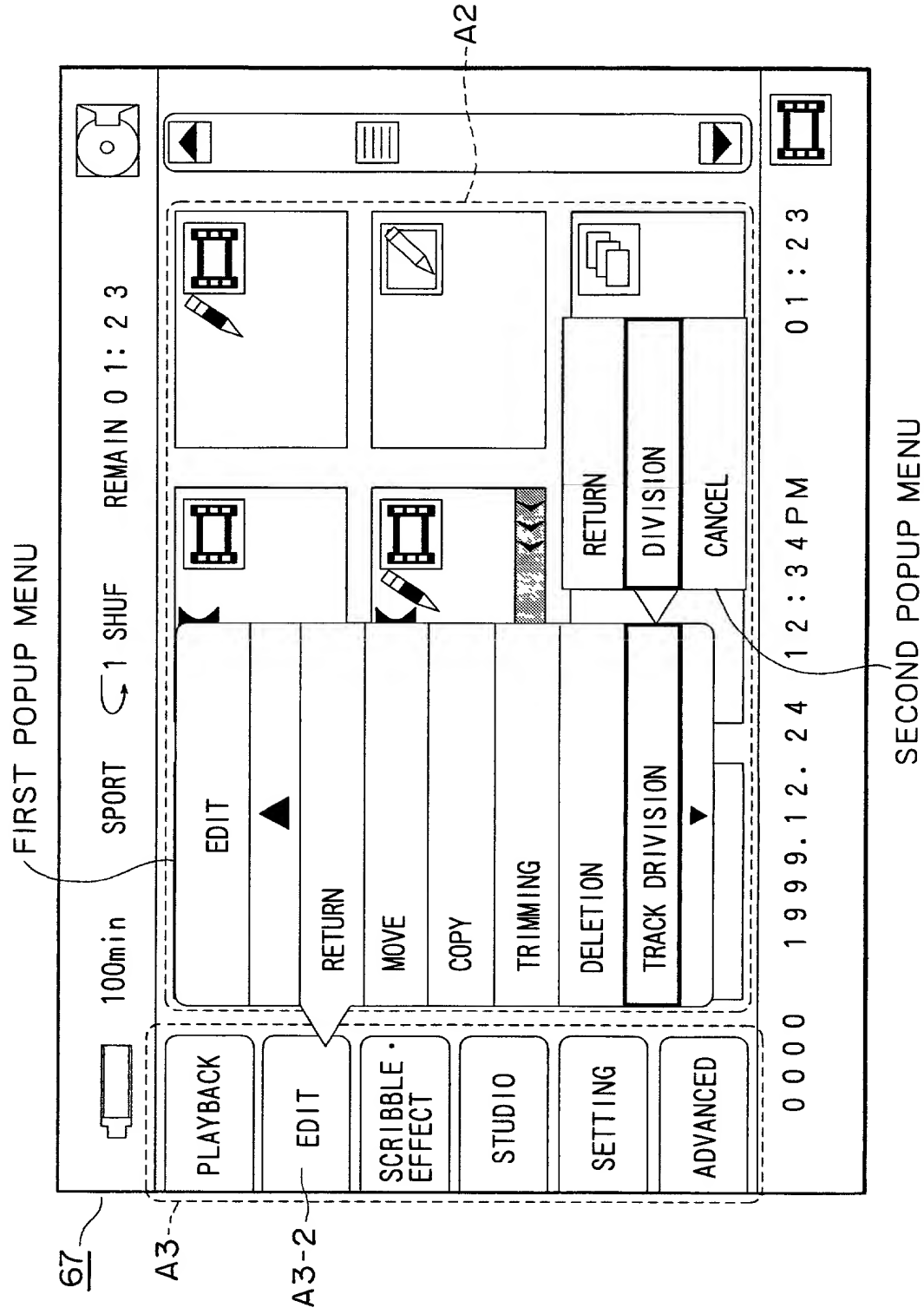
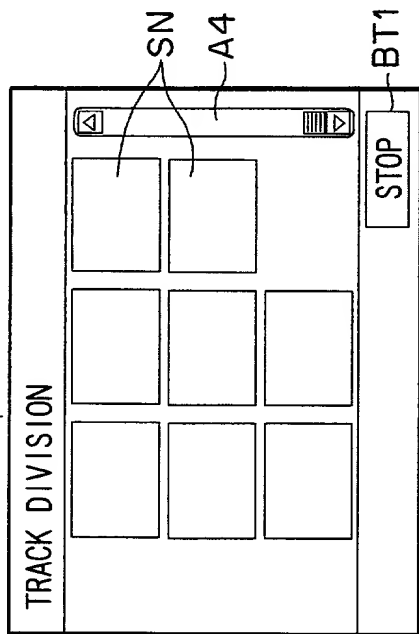


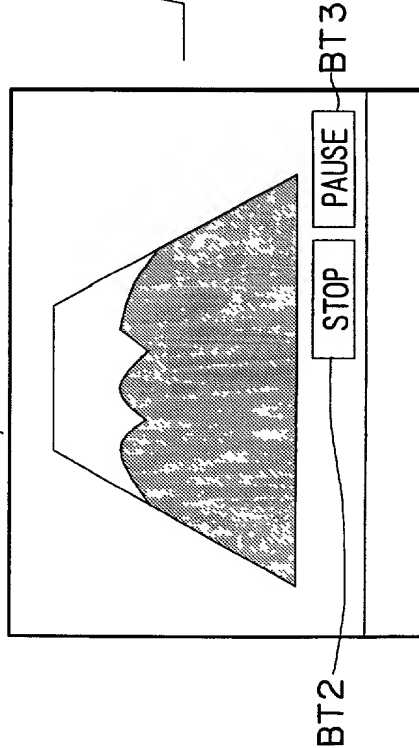
FIG. 16A 67



[SELECT THUMBNAIL IMAGE TO BE DIVIDED]

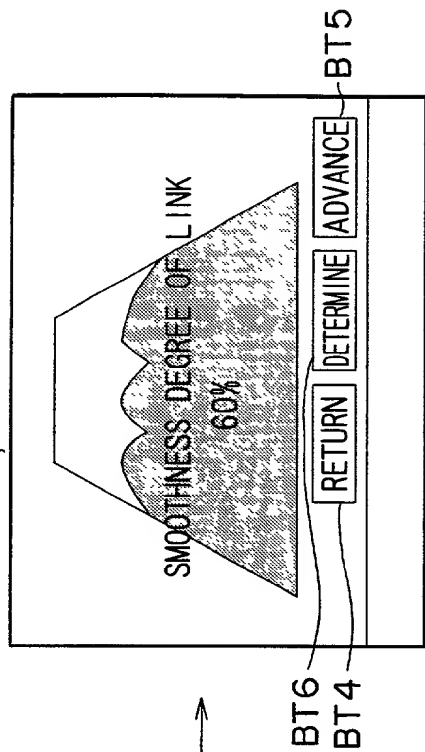
[REPEAT PLAYBACK]

FIG. 16B 67



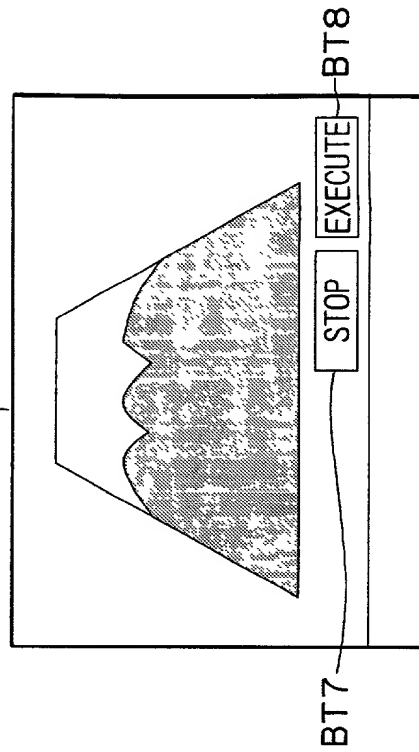
[OPERATE "PAUSE" AT POSITION FOR DIVISION]

FIG. 16C 67



[FINE ADJUSTMENT BY OPERATION OF "RETURN" OR "ADVANCE"]

FIG. 16D 67



[EXECUTE DIVISION BY OPERATION OF "EXECUTE"]

FIG.17

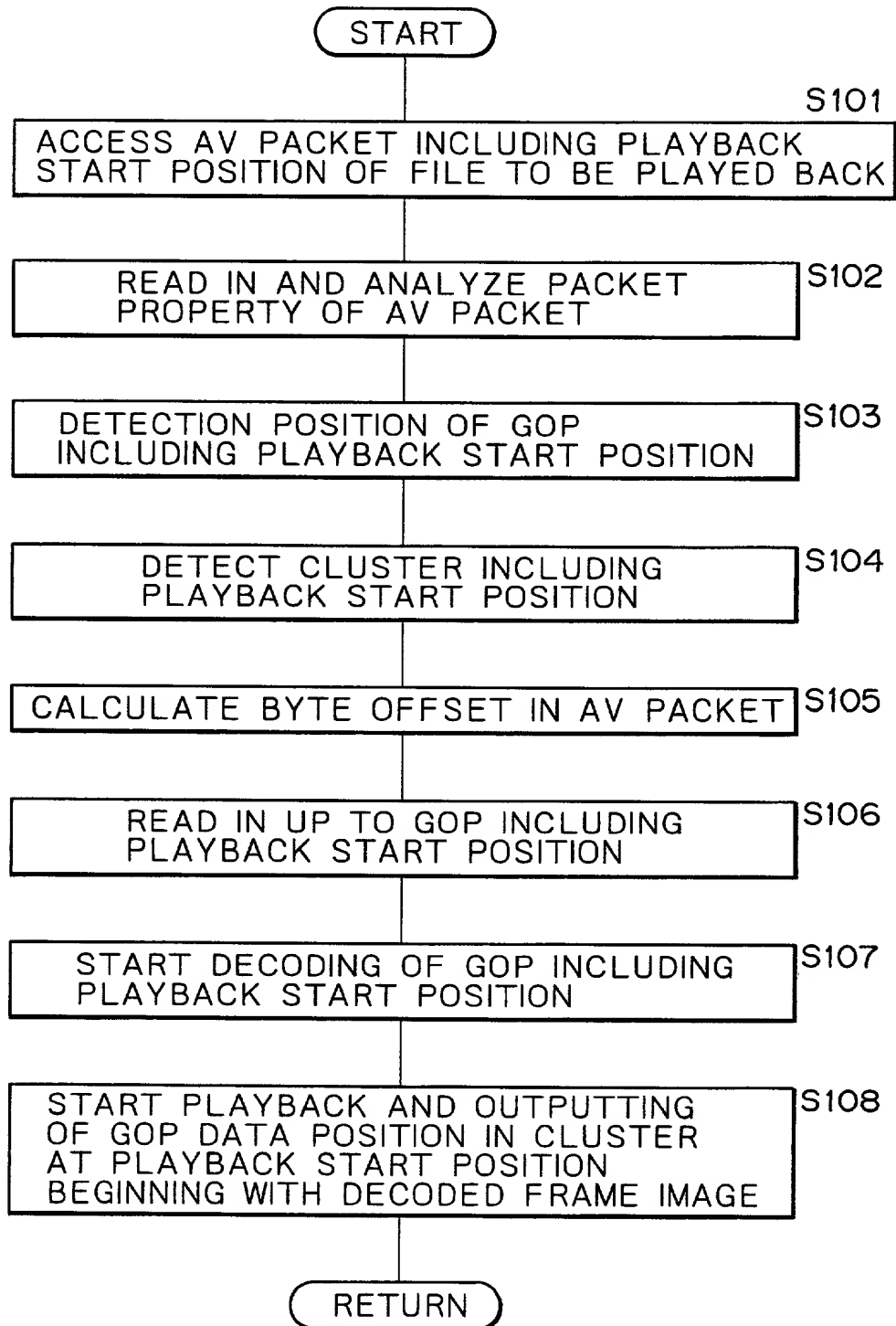


FIG. 18

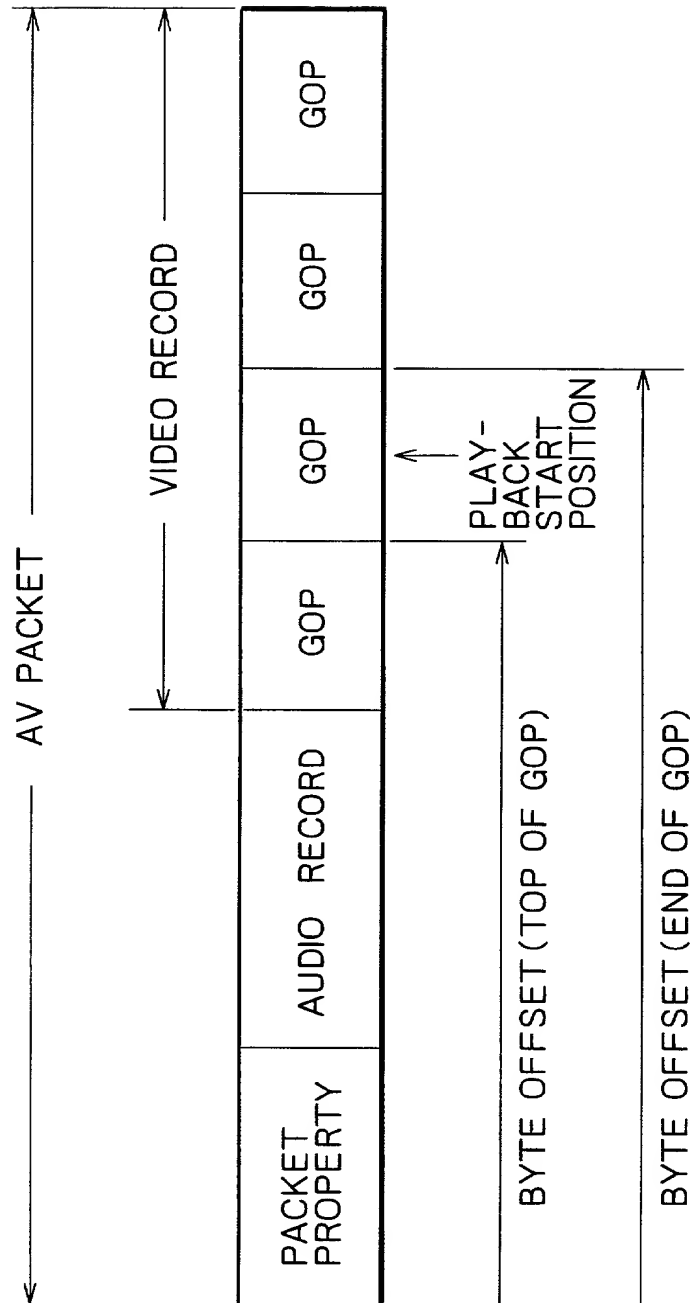


FIG. 19

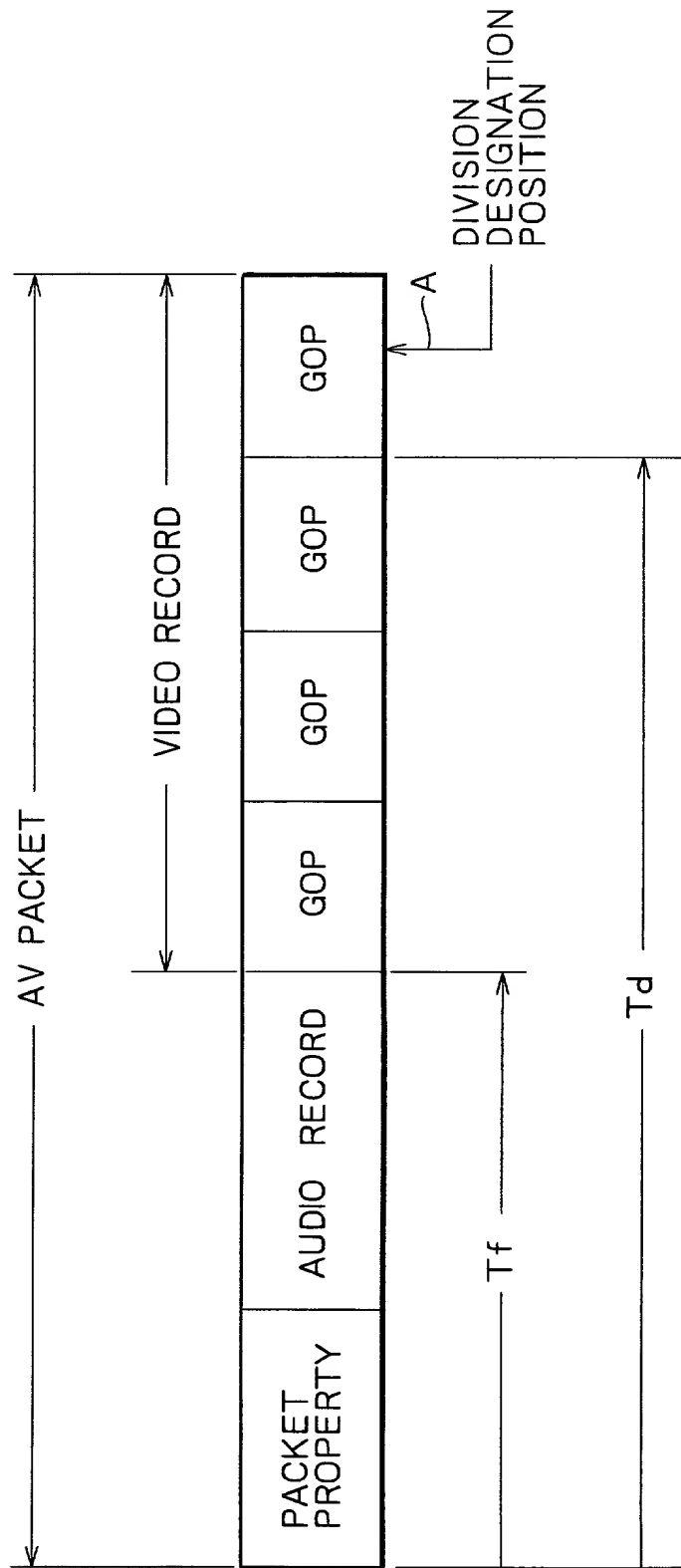


FIG. 20

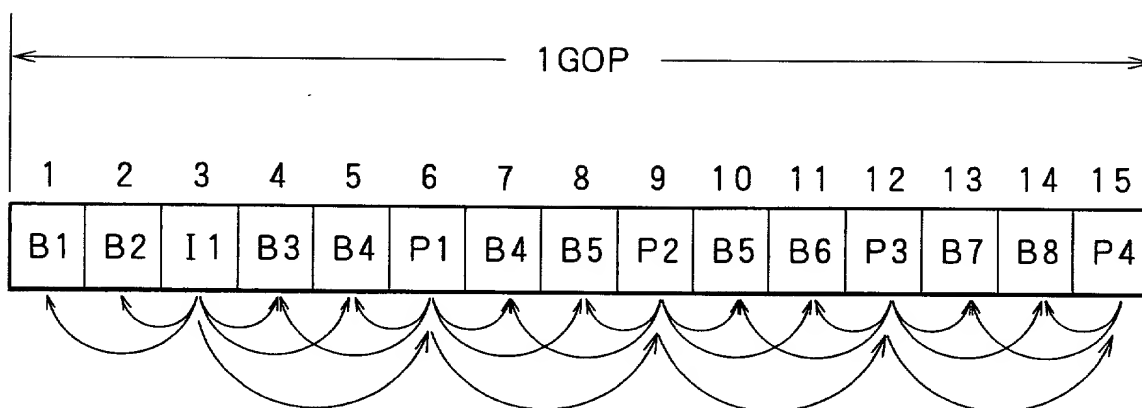


FIG. 21A

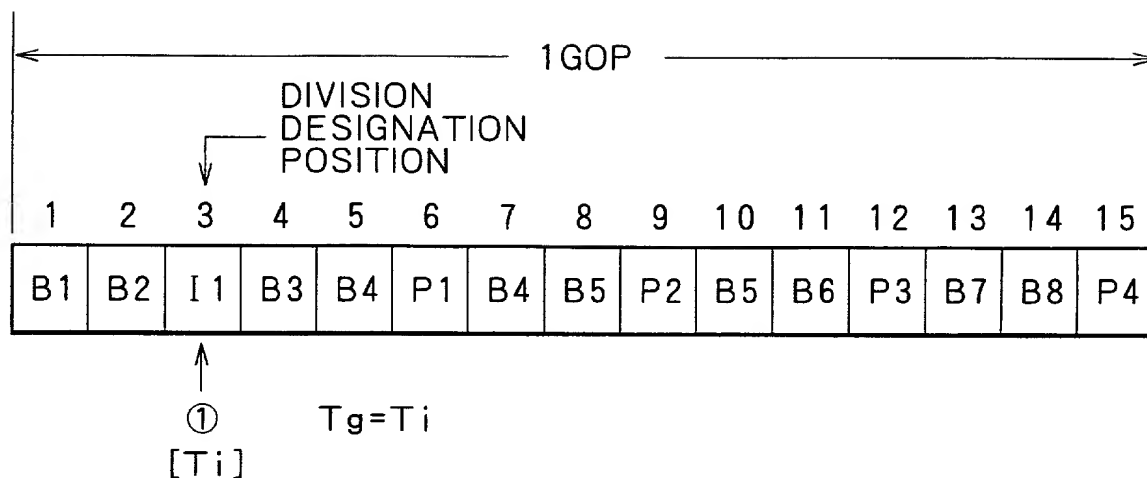


FIG. 21B

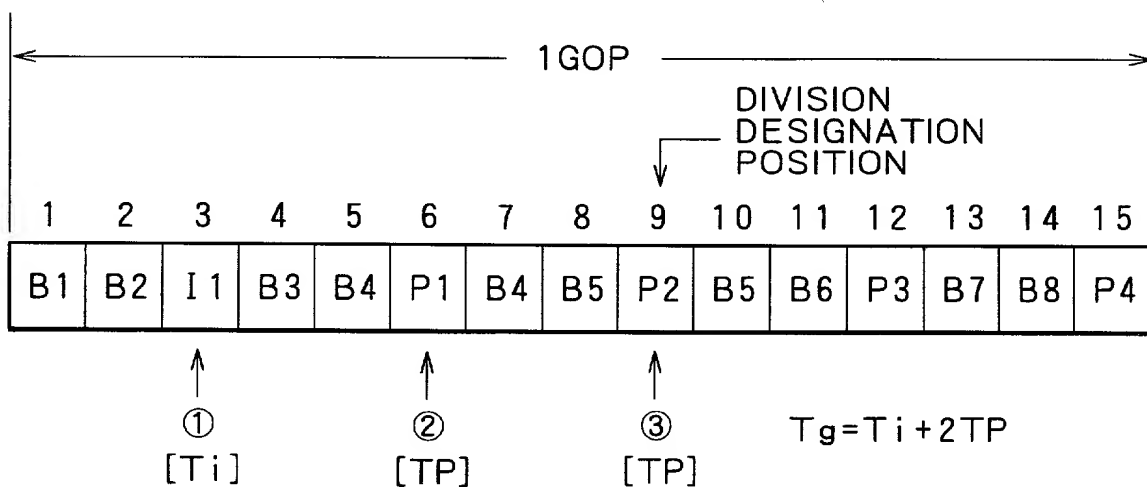


FIG. 21C

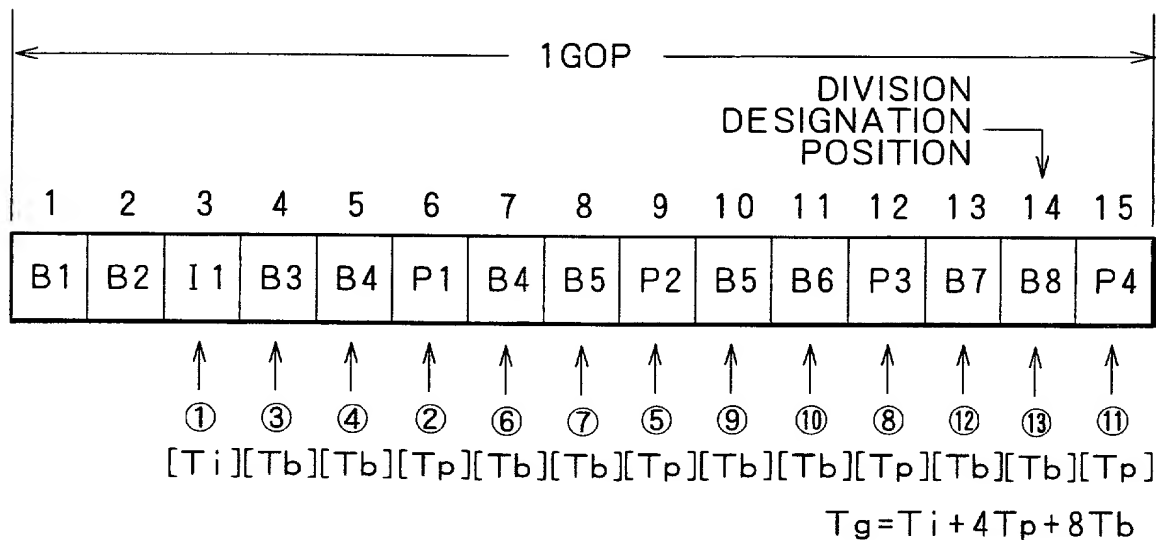


FIG. 22

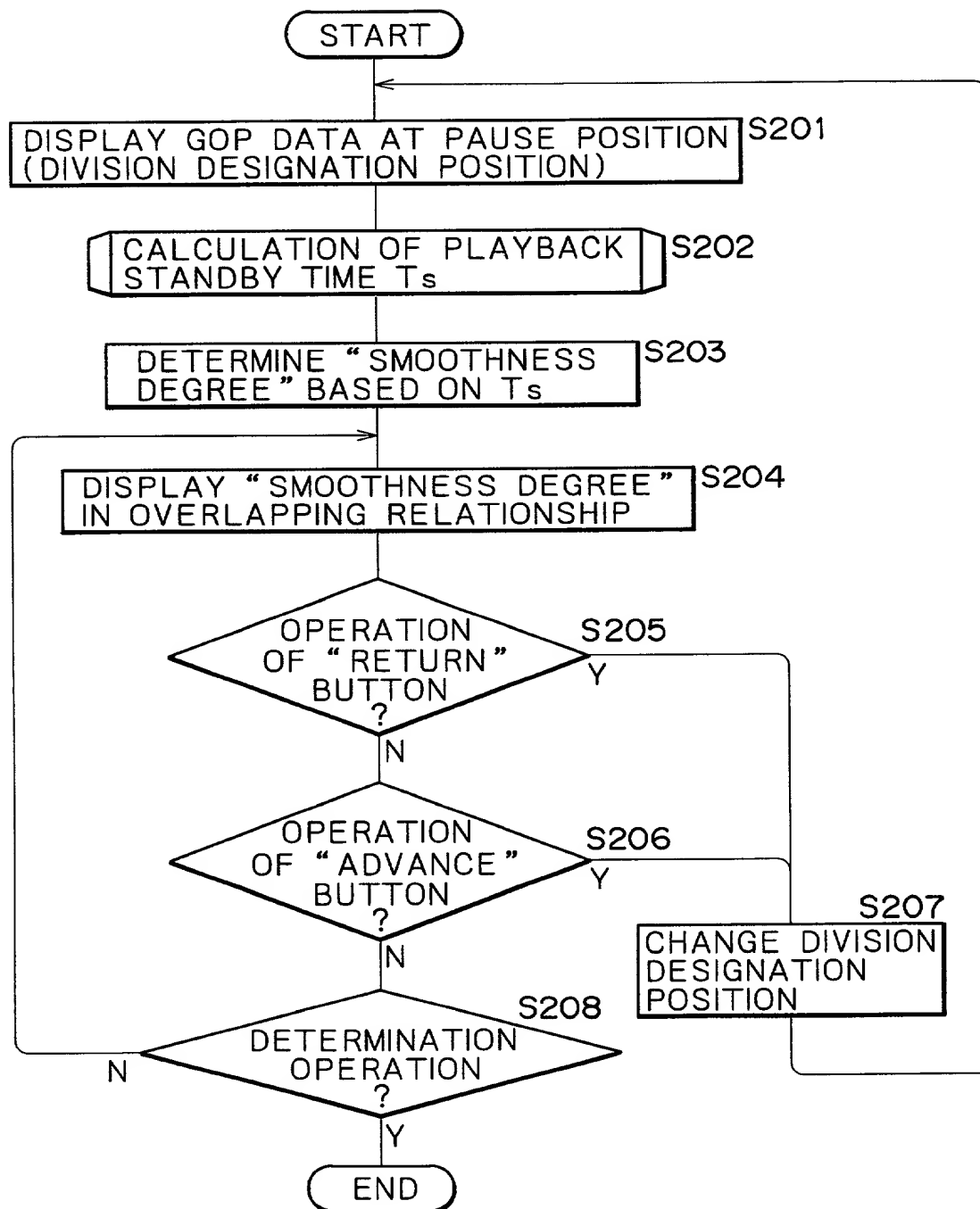


FIG. 23

